



**Dr B R AMBEDKAR NATIONAL INSTITUTE OF TECHNOLOGY, JALANDHAR**  
**G T Road By Pass, Jalandhar-144008, Punjab (India)**  
**(An Institute of National Importance)**

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**Admission to Post Doctoral Fellowship (PDF) (Full-Time) under**  
**Visvesvaraya PhD Scheme Phase –II**

Applications are invited for admission to PDF (Full-Time) Programme under Visvesvaraya PhD Scheme Phase-II. The Broad areas of Research, Eligibility Criteria, Selection Procedure, Teaching Assistantship-cum-Scholarships etc. are given in this advertisement.

**How to apply:** The link to fill the online application form can be accessed through Institute website [www.nitj.ac.in](http://www.nitj.ac.in). An application fee of Rs.1000/- (Rs.500/- for SC/ST/PWD candidates) is required to be paid online. The candidates are required to apply online and there is no requirement to send the Hard Copy of the application form to the Institute. The candidates have to upload a scanned copy of the duly filled in **Annexure-I** alongwith other documents in a single pdf file as per the application portal. Application fee once submitted shall not be refunded in any case.

**Last date for submission of online application form is 21/07/2025.**

For more details, the candidates are advised to visit the Institute website [www.nitj.ac.in](http://www.nitj.ac.in) for updates. All updates regarding this Advertisement will be updated on the Institute website only. All candidates are advised to visit the Institute website regularly.

**Contact Person**

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**Nodal Officer**  
**Visvesvaraya PhD Scheme**  
**Department of Electronics and Communication Engineering**  
**Dr B R Ambedkar National Institute of Technology, Jalandhar,**  
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**Admission to PDF (Full-Time) Programme under Visvesvaraya PhD  
Scheme Phase –II**

**A) Broad Areas of Research**

| <b>Sr. No.</b> | <b>Department<br/>Email address of HOD</b>  | <b>(Broad Areas of Research)</b>  | <b>Number of Seats</b> |
|----------------|---|---|------------------------|
| <b>01</b>      | Electronics & Communication Engineering<br><b>Email:</b> <a href="mailto:hec@nitj.ac.in">hec@nitj.ac.in</a> | ESDM: Semi-Conductor, RF Design, Circuits Design and Fabrication, Antenna Design, ECE | <b>01</b>              |

**B) Eligibility Criteria**

**The eligibility criteria for the minimum educational qualification will be as under:**

- Master's Degree in Engineering/Technology in the relevant area of research along with Bachelor's Degree in appropriate branch of Engineering/Technology with a first class or minimum 60% marks (or CGPA of 6.5 on 10-point scale) in the qualifying examination. **Gate Score and PhD Degree is mandatory.**

**C. Terms & Conditions for the Visvesvaraya PhD Scheme – Post Doctoral Fellowship (PDF)**

**1. Eligibility and Duration**

1.1 The PDF is awarded to researchers with a proven academic record, including peer-reviewed publications and recognitions.

1.2 It is a full-time, non-transferable, temporary position tenable in India only.

1.3 The fellowship is valid for **1 year**.

1.4 The PDF applicant should be an Indian citizen.

1.5 PDF applicants currently in regular employment will not be considered. However, scientists or researchers with temporary positions in academia or research institutions would be considered, but they would be required to relinquish their current roles if selected for the fellowship.

1.6 The PDF applicant must have obtained a PhD degree from a recognized University with first class (in terms of grades, etc.) in all preceding levels and a good academic record throughout.

1.7 The eligible participating institution must ensure that the PDF applicant should have completed **PhD within the past 5 years** on the last date of submission of application to the institution.

1.8 PDF applicants should not have completed their PhD at the same institution (host institution) where the PDF fellowship is to be awarded.

1.9 The **upper age limit for the fellowship is 40 years** on the date of submission of application to the eligible participating institution (host institution).

1.10 A suitable Mentor/Guide, under whom the proposed research would be carried out, must be identified by the institution.

1.11 Mentor/Guide of PDF fellow must hold a regular academic/research position in a recognized institution in India. He/she should hold a Ph.D. degree in Science or Engineering.

1.12 The research proposal of the PDF applicant, to be submitted to the institution, should define clear objectives, outcomes and deliverables against the award.

1.13 Preference would be given to the PDF applicants who have active industry linkages and their research proposal is directed towards industry specific problems.

## **2. Responsibilities of the PDF Awardees**

2.1 In addition to own research work, the awardees need to participate in activities under the Visvesvaraya PhD Scheme as assigned by PhD Cell such as:

- Evaluation of research work of PhD Candidates
- Mentoring of PhD candidates
- Participation in collaborative research under the scheme
- Participation in workshops and events
- Other assignments by PhD Cell

2.2 Provide **research proposals** including specific timelines, deliverables, etc. and **monthly progress reports**.

2.3 Report significant achievements during the tenure

2.4 Promptly follow the directions of PhD Cell

2.5 Acknowledge the scheme at all significant forums including publications/achievements/recognitions etc. using the following template:

*“This publication/achievement/recognition is an outcome of the R&D work undertaken during the tenure of PDF award under the Visvesvaraya PhD Scheme, being implemented by PhD Cell, Digital India Corporation, MeitY.”*

2.6 Inform the PhD Cell **at least one month in advance** for any **long-term leave** (e.g., study leave, sabbatical, etc.).

2.7 Must **not receive any other fellowship** during the award period.

2.8 If any awardee wishes to discontinue the fellowship, the institution should inform PhD Cell, DIC; MeitY immediately. One month's notice is to be given by the institution before the date of discontinuation.

## **3. Financial Support**

- Fellowship: ₹1,08,393/month
- Contingency Grant: ₹1,00,000/year (disbursed on a **pro-rata** basis, e.g. ₹25,000/quarter)
- Contingency Grant usage is restricted to:
  - Minor equipment
  - Consumables
  - Research-related items
  - No provision for hiring research staff

## **D. Standard Operating Procedure (SOP) for Post Doctoral Fellowship (PDF) Awardee and Host Institution**

### **1. Onboarding and Reporting**

The host institution must ensure timely selection of the awardees (within the timelines as specified by PhD Cell) against the allotted seats, following the scheme guidelines and their timely reporting to the PhD Cell, Digital India Corporation (DIC), MeitY, for registration under the scheme.

1.1 Awardees not registered under the scheme will not be eligible for support.

1.2 The institution must notify the PhD Cell immediately about any dropouts or termination of PDF awardees.

### **2. Progress and Research Reporting**

During registration under the scheme, the awardees need to submit proposals including specific timelines, deliverables, etc. Subsequently, the PDF awardees must submit monthly progress reports in the format specified by the PhD Cell. They must also update the PhD Cell on any publications, patents, start-ups, or other outputs resulting from their research.

Institutions are required to ensure timely and accurate submission of these reports.

### **3. Financial Support**

3.1 The fellowship amount of ₹1,08,393/- per month will be transferred directly to the PDF awardee's bank account. Contingency Grant of ₹1.00 lakh per year will be calculated on a pro-rata basis (e.g. ₹0.25 lakh per quarter).

3.2 In case of non-performance, non-compliance with the scheme's guidelines, absence, leaves, etc. or other reasons, affecting the Fellowship amount or resulting into Fellowship stoppage of any PDF awardee at any point of time, the institution shall intimate PhD Cell, DIC regarding the same immediately through email to PhD Cell. The institution needs to secure receipts of such intimation by PhD Cell, DIC well in advance as the fellowship would be released by DIC, as soon as possible, once the fellowship becomes due.

3.3 In absence of such intimation, PhD Cell, DIC will continue to transfer the fellowship directly to the PDF awardee every month of a financial year.

3.4 If due to non-intimation or late intimation by the respective institute, the fellowship is released to the PDF awardee by PhD Cell, DIC then it will be the responsibility of the institute to recover that amount paid to the candidate in excess & beyond eligibility. PhD Cell, DIC in its sole discretion may decide to recover it from the amount due to be paid to the institute under other budget heads of the scheme.

3.5 The awardee must submit a detailed proposal with planned expenditure from Contingency budget head, in the prescribed format. Reimbursements may also be considered, subject to expenditure, per scheme guidelines. Expenditure beyond pro-rata eligibility will not be considered.

3.6 The host institution must submit Utilization Certificates (UCs), Statement of Accounts (SoAs), and Statement of Expenditure (SoEs) along with requests for the next quarter's fund release. Unspent balances and interest, if any, must be refunded to the PhD Cell.

### **4. Completion and Exit Formalities**

Upon completion of the fellowship, the institution must submit a final completion report and all requisite documents. The format for the final report will be shared by the PhD Cell.

## 5. Compliance and Termination

5.1 Both the institution and the PDF awardee are required to adhere to the scheme guidelines and instructions, issued periodically by the PhD Cell.

5.2 The PhD Cell reserves the right to hold, stop, or terminate support for the PDF award at any stage due to reasons including, but not limited to, inadequate progress, non-compliance with guidelines, or any other reason deemed appropriate.

### E) Important Dates

| Sr. No. | Activity  | Date                                 |
|---------|---|--------------------------------------|
| 1.      | Last date for submission of Applications                              | <b>21/07/2025</b>                    |
| 2.      | Display of list of candidates eligible for presentation & interaction | <b>22/07/2025</b>                    |
| 3.      | Presentation/interaction by all eligible candidates                   | <b>28/07/2025 at 10.00 AM</b>        |
| 4.      | Display of list of selected candidates                                | <b>29/07/2025</b>                    |
| 5.      | Deposition of fee & Registration                                      | <b>30/07/2025</b>                    |
| 7.      | Last date of fee submission & Registration                            | <b>As notified by the Department</b> |

All the dates mentioned above are tentative subject to change at any stage by the competent authority. All the candidates interested in seeking admission are requested to visit the Institute website regularly for updates.

- f) All disputes pertaining to the admissions shall fall within the jurisdiction of Jalandhar only.
- g) All admissions will be provisional till these are confirmed subject to medical fitness, payment of all the fees, fulfillment of eligibility conditions, and verifications of certificates by the Academic Section of the Institute.
- h) After payment of fee (application/admission fee), no fee refund request shall be entertained in any case.
- i) The candidates have to upload a scanned copy of the duly filled in **Annexure-I** alongwith other documents on the online application portal.
- j) The Institute reserves the right to modify or cancel this Advertisement/any part of this Advertisement at any stage.

-Sd/-  
Dean Academic

## Annexure-I

### Application Form [Post-doctoral Fellowship under Visvesvaraya PhD Scheme for Electronics and IT: Phase II]

In the broad area of research: **Semiconductor, RF Design, Circuits Design and Fabrication, Antenna Design, ECE**

Number of Seats: 01 (ONE)

#### Instructions:

- All entries should be typed / written in capital letters only.
- \*Strike out whichever is not applicable.

Paste recent passport size photograph

1. Full name of the candidate: .....

(as per last qualifying examination mark sheet / grade card)

2. Father's Name :.....

3. Mother's Name :.....

4. Address for  
Communication :

\_\_\_\_\_

Mob. :

\_\_\_\_\_

Email :

\_\_\_\_\_

5. Permanent  
Address :

\_\_\_\_\_

Ph (STD):

\_\_\_\_\_ Mob:

\_\_\_\_\_

6. Date of Birth : \_\_\_\_\_(DD/MM/YY)

7. Category (Open/OBC/SC/ST/EWS/PWD) :

(Attach category proof)

\_\_\_\_\_

**8. Academic Record (Attach all educational grade-sheets and certificates)**

| Sl. No. | NAME OF EXAMINATION PASSED | DURATION (MM/YYYY) |              | NAME OF THE INSTITUTE/ UNIVERSITY | DIVISION / PERCENTAGE OF MARKS / CPI/CGPA |
|---------|----------------------------|--------------------|--------------|-----------------------------------|---|
|         |                            | START DATE         | PASSING DATE |                                   |   |
| 10th    |                            |                    |              |                                   |   |
| 12th    |                            |                    |              |                                   |   |
| UG      |                            |                    |              |                                   |   |
| PG      |                            |                    |              |                                   |   |
| Ph. D.  |                            |                    |              |                                   |   |

**9. Title of PhD Thesis**

**10. Date of Accruing the PhD Degree/Notification:**

**11. Details of GATE Score (attach) :.....**

**8. Mention the area of proposed research**

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**9. Recent bio-data with following**

**A. Details of Publications (SCI/SCIE), Patents, Product Development, Transfer of Technology, if any (attach separate sheet if needed.)**

**Patent(s) Details**

|  | Patent 1 | Patent 2 | Patent 3 | Patent 4 | Patent n... |
|--|----------|----------|----------|----------|-------------|
| <b>Title</b>                                 |          |          |          |          |             |
| <b>Status (Filed/Published/Granted etc.)</b> |          |          |          |          |             |
| <b>Countries where it is filed</b>           |          |          |          |          |             |

|                               |  |  |  |  |  |
|-------------------------------|--|--|--|--|--|
| <b>Publication Number</b>     |  |  |  |  |  |
| <b>Link to the patent</b>     |  |  |  |  |  |
| <b>Field of the invention</b> |  |  |  |  |  |
| <b>Inventors</b>              |  |  |  |  |  |
| <b>Abstract</b>               |  |  |  |  |  |
| <b>Other details</b>          |  |  |  |  |  |

**Publication(s) details of Journals**

|                            | <b>No. of publications</b> | <b>List with DoI no.</b> |
|----------------------------|----------------------------|--------------------------|
| <b>SCI Publications</b>    |                            | 1.                       |
| <b>Scopus Publications</b> |                            | 1.                       |

**Publication(s) details of Conference/Seminar**

|  | <b>No. of publications</b> | <b>List with DoI no.</b> |
|--|----------------------------|--------------------------|
| <u>International Professional Society Conducted like IEEE/ASME/ACE etc. Paper(s) Presented and Published in Proceeding</u>   |                            |                          |
| <u>International Professional Society Conducted like IEEE/ASME/ACE etc. Paper(s) Presented but not Published in Proceeding</u>   |                            |                          |
| <u>International/National Elsevier or Springer sponsored conference/Seminar supported by National/Regional professional body. Paper(s) presented and published in proceeding</u> |                            |                          |
| <u>Others Including Conference/Seminar. Paper(s) Presented and Published in Proceeding</u>   |                            |                          |



|  |  |  |
|--|--|--|
|  |  |  |
|--|--|--|

**Startup Details (if, any)**

|                    | Startup 1 | Add columns for more startups..... |
|--------------------|-----------|------------------------------------|
| <b>Name</b>        |           |                                    |
| <b>CIN</b>         |           |                                    |
| <b>Website</b>     |           |                                    |
| <b>Description</b> |           |                                    |

**Technology and Product Developed (if, any)**

|  | 1 | 2 | Add columns for more ..... |
|--|---|---|----------------------------|
| <b>Short Description</b>                         |   |   |                            |
| <b>Status (whether commercialized /deployed)</b> |   |   |                            |

**B. Three Best Research Papers Published (attach relevant copy)**

**10. Provide Research Proposal including objectives, deliverables, rationale, scientific & industrial merit, industry linkages, minor equipments, consumables, specific time-lines @ NIT Jalandhar** (Attach Separate Sheets)

**11. Work Experience with relevant certificates**

**12. DECLARATION BY THE CANDIDATE:** I hereby declare that the information submitted above is correct. If selected, I promise to abide by rules and regulations of the institute in force and amended from time to time. I have read the terms and conditions & standard operating procedure (SOP) of the Scheme for which I am applying and promise to abide by that.

Date:

(Signature of the Candidate)

| S. No | Name of Faculty             | Email ID   | Area of research<br>(Must align with the areas given in Vishveshvara portal) |
|-------|-----------------------------|--|--|
| 1     | Prof Binod Kumar Kanaujia   | Prof Binod Kumar Kanaujia                                | Circuit design and fabrication   |
| 2     | Dr Ashish Raman             | ramana@nitj.ac.in  | VLSI design, Circuit design and Fabrication                                  |
| 3     | Dr Rohit Singh              | rohits@nitj.ac.in  | Artificial Intelligence and Circuit Design                                   |
| 4     | Dr. Bodile Roshan Mukindrao | mukindraobr@nitj.ac.in                                   | AI and Quantum Computing   |
| 5     | Dr. Asutosh Kar             | kara@nitj.ac.in  | AI, Machine Learning, Signal Processing                                      |
| 6     | Sukwinder Singh             | sukwinders@nitj.ac.in                                    | Circuits Design and Fabrication  |
| 7     | Ramesh K Sunkaria           | sunkariark@nitj.ac.in                                    | Biomedical Signal Processing application with Artificial Intelligence        |
| 8     | Dr Indu Saini               | sainii@nitj.ac.in  | Artificial intelligence  |
| 9     | Neetu Sood                  | Soodn@nitj.ac.in   | Artificial Intelligence  |
| 10    | B S Saini                   | sainibs@nitj.ac.in                                       | Biomedical Signal and Image Processing                                       |
| 11    | Aijaz Mehdi Zaidi           | zaidiam@nitj.ac.in                                       | Circuits Design  |
| 12    | Dr Manjeet Singh            | <a href="mailto:singhm@nitj.ac.in">singhm@nitj.ac.in</a> | AI   |
| 13    | Dr Balwinder Raj            | rajb@nitj.ac.in  | Semi Conductor Process, Circuits Design and Fabrication                      |
| 14    | Dr. Kundan Kumar            | kumarkundan@nitj.ac.in                                   | Circuits Design and Fabrication, Semi Conductor, Material Technology         |
| 15    | Dr. Pawan Kumar Verma       | vermapk@nitj.ac.in                                       | Artificial Intelligence  |
| 16    | Dr. Nitesh Kashyap          | kashyapn@nitj.ac.in                                      | Circuit Design, AI   |
| 17    | Dr Deepti Kakkar            | kakkard@nitj.ac.in                                       | AI   |
| 18    | Dr Tarun Chaudhary          | chaudharyt@nitj.ac.in                                    | Circuit Design   |
| 19    | Dr Arun Khosla              | khoslaak@nitj.ac.in                                      | AI   |