

Self-Sponsored One-Week Short Term Course On

Current Trends in Submicron Device and Circuit Design

(26-30 June, 2023)

Organized by

Department of Electronics & Communication
Engineering



Dr B R Ambedkar

National Institute of Technology, Jalandhar
(Punjab)-144011

Website: www.nitj.ac.in

About NIT, Jalandhar

Dr B R Ambedkar National Institute of Technology, Jalandhar was established as Regional Engineering College in the year 1987. It was given the status of National Institute of Technology in 2002 and functions under the aegis of Ministry of Human Resource Development. As an "Institute of National Importance" it imparts high quality technical education in Engineering, Technology and Science to produce competent technical manpower for the country. The institute offers B. Tech programmes in nine engineering disciplines. Four new disciplines (Mining Engineering, Electrical Engineering, Fashion Technology and Information Technology) have started from 2013-14 academic session. In addition to B. Tech programmes, the institute offers M Tech, Ph. D. programmes in all Engineering disciplines.

About the Department

The Department of Electronics & Communication Engineering, Dr. B R Ambedkar NIT Jalandhar has been offering B.Tech programme in Electronics & Communication Engineering since 1989, M.Tech programme in Electronics & Communication Engineering (Full-Time) and VLSI Design (Full Time) from year 2007. The Department also offers Ph.D. programmes in various specializations, such as Biomedical Signal and Image Processing, Digital Signal and Image Processing and Its Applications, VLSI Design and Communication Systems etc. The course curriculum has been designed compatible to the existing and emerging needs of the industry. The Department has established state-of-the-art laboratories for B.Tech, M.Tech and Ph.D. research work. The Department has advanced Medical Imaging and Computational Modeling of Physiological Systems Research Laboratory Biomedical Signal Processing and Telemedicine Laboratory. More than thirty-five students have already completed their Ph.D. degree and presently more than thirty research scholars are registered in the department for their Ph.D. degree.

Aims of the STC

1. To enhance technical and professional competency of the faculty members.
2. To promote interaction among the professionals working in applications of Nanoelectronics and VLSI Design and allied areas.

Course Contents of Short Term Course

Challenges in Sub micrometer Design
Nanoscale Semiconductor Device Design
MOS Transistor and its Modeling Proscriptive
Advanced VLSI Circuits Design
Analog and Digital Circuit Design
Familiarization with VLSI Tools
Practical Application and Case study

Resource Persons

The faculty and resource persons for the course will be from reputed educational, research organizations and industry.

General Information

Registration Fee:1

For Internal: Faculty: Rs. 200;

Students/Research Scholar: Rs. 200

For External :

Faculty/Industrialist: Rs. 200 + 18% GST

Students/Research Scholar: Rs. 200 + 18% GST

Last date of registration is: 21.06.2023

Chief Patron

Professor (Dr) Binod Kumar Kanaujia

Director,

Dr B R Ambedkar National Institute of Technology Jalandhar
(Punjab).

Head of the Department

Dr. Ramesh K. Sunkaria

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Course Coordinators

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Registration Link:

https://www.nitj.ac.in/events_registration/stc_sdcd/login

Note:

1) Prior registration is compulsory to attend the STC.

2) E-Certificate will be issued to the participants on successful participation to the STC.

3) The Google Meet link will be shared with the registered participants.

Contact Details:

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