

VLSI DESIGN

Six Weeks Online Summer training program

VLSI Design Techniques are the central pillar of most of the present research in industrial and commercial activities in Electronics Engineering. This training program focuses on specific circuit design techniques using Xilinx Vivado Design Suite and using Cadence EDA tools. It also focuses on embedded system design based on 8051 Microcontroller using Kiel μ Vision.

The main objective of this training program are as follows: -

- To expose the participants to various modelling, simulation, and design techniques, applied to contemporary problems in the field of VLSI Design and Embedded Systems.
- To train the Engineers and faculty members with latest software and hardware.
- To provide a forum to exchange and stimulate new ideas.

Duration: 06 Weeks

Training Program Content

Introduction to digital circuits and VHDL

- Introduction to VLSI Design flow
- Various Modeling styles and Data types of Verilog/VHDL

CMOS Introduction

- Introduction to MOS transistors and CMOS Logic
- MOS Inverters: Switching characteristics and Interconnect effect
- CMOS layout design
- Design layout of CMOS Inverter, CMOS NAND, and CMOS NOR

Embedded System Design

- Introduction to Microcontroller and Microprocessor.
- Embedded System interfacing and programming of 8051 Microcontroller.
- Application development using Keil μ Vision.

Digital System Design using FPGAs

- Introduction to Xilinx FPGA Kit
- FPGA Based Design Flow
- FSM based Modelling of Digital Circuits

Project: Design and Implementation of Digital Circuits

Registration Details

There are limited seats for 100 participants in the training program which would be filled on first come first serve basis.

External Students: Rs 2500 (INR)	Registration Deadline: 10-06-2021 June 30, 2021 Participation Confirmation: 18-06-2021 July 5, 2021
Internal Students: Rs 1000 (INR)	
Faculty Member: Rs 3000 (INR)	

How to Apply

Fill the Google Form on the given link

<https://forms.gle/dRQeFvRai3nyfkvT6>

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