### DR B R AMBEDKAR NATIONAL INSTITUTE OF TECHNOLOGY



(Institute of National Importance) G T Road Bye pass, Jalandhar -144011, Punjab (India)

**File No.**: EE-9/2/2021-R&D-E Date :26/08/2023

## Project Associate and Sr. Project Associate position under MeitY Funded Project, Government of India

Applications are invited under the sponsored project. The details of the project are as under:

| Position                    | Project Associate (01), Sr. Project Associate (01)  |
|-----------------------------|---|
| Number of Position (s)      | Two (02)  |
| Title of The Project        | Design and development of System on Chip based next generation IoT System   |
|                             | for Industry 4.0 with Functional Safety and Security Features   |
| Chief Investigator          | <b>Dr. Ashish Raman</b> (Associate Professor), Department: Electronics and Communication Engineering Department, NIT Jalandhar, Grand Trunk road, Barnala-Amritsar, Bypass road, Jalandhar, Punjab 144008, E-mail:ramana@nitj.ac.in, M: +91-9888217273  |
| Tenure of Project           | The position is purely temporary and co-terminus with the project which is sanctioned up to May 2027 or till the completion of the project.   |
| Essential Qualification     | Project Associate: BE/B.Tech (ECE or Relevant Branch) and ME / M.Tech in VLSI / Microelectronics / Nanoelectronics / Electronic System and related areas with minimum of first class from a reputed institute or In case class is not specified in degree then minimum 60% marks or 7.0 CGPA will be considered as 1 <sup>st</sup> class. Preference should be given to GATE/NET qualified. Candidate must have good knowledge is VLSI/TCAD design tool i.e. Cadence, Synopsys, Mentor Graphics etc is desirable. Additional Terms and conditions as per funding agency Guidelines.  Sr. Project Associate: In addition of above, the preference is to be given to PhD degree/Thesis submitted or M.Tech with experienced person. |
| Desirable                   | <b>Project Associate:</b> Working knowledge of TCAD/CADANCE/SYNOPSYS /MATLAB and Chip Tap out will be an added advantage. <b>Sr. Project Associate:</b> As above with desirable experience with 01 – 03 years with working knowledge in the field of VLSI Design.   |
| Age and Relaxation (if any) | Age relaxation for SC/ST/OBC/PH/Female candidates as per GoI rules  |
| Fellowship/Salary           | Project Associate: Rs. 45,000/- (Per month) and 10 % Increment per year. Sr. Project Associate: Rs. 54,000/- (Per month) and 10 % Increment per year.   |
| Last Date & Time            | Eligible candidates may apply by sending their detailed Bio-data and copies of educational qualifications, age proof, and proof of research experience (if any), valid cast certificate (if applicable) and annexure I, should reach to the Principle Investigator by E-mail ( <a href="maina@nitj.ac.in">ramana@nitj.ac.in</a> ) in soft copy on or before 10/09/2023. The e-mail subject line should read as "Application for the Post of Project Associate/ Sr. Project Associate in MeitY Project"  |
| Date of Interview:          | To be notify (Online Mode)  |
| Reporting Time, Venue:-     | Online (Link will be provided at the time of interview)   |

- Selected candidate may register for a Ph.D. programme under CI in ECE department, NIT Jalandhar as per the NITJ PhD rules and regulations.
- The appointment of the staff member will be purely temporary/ ad-hoc/contractual basis and valid for the above period as mentioned above or until the services are required by the Project PI, whichever is earlier.
- The services of the staff member will be liable to termination at any time without any notice and without assigning any reason.
- The appointment shall be specifically for the aforesaid Project and staff member will not have any claim / right on any post in the Institute/University.
- Shortlisted candidates will be informed through email on the date of interview. Mere possession of minimum qualification does not guarantee an invitation to the interview. Candidates will be short listed based on their merit and as per the requirement of the project. All candidates should make their own arrangements for their stay, if required. No TA/DA will be paid to attend the interview.

# Application Format for Project Associate and Sr. Project Associate to work in the MeitY sponsored project

#### 1. PersonalDetails

| Full Name (In capital)   |                     |  |  |  |  |  |
|--|---------------------|--|--|--|--|--|
| Date of Birth* (DD/MM/YY)  |                     |  |  |  |  |  |
| Category(General/SC/ST/OBC)  |                     |  |  |  |  |  |
| Age(inyears)   | Gender(Male/Female) |  |  |  |  |  |
| Marital Status (Single/Married)  | Nationality         |  |  |  |  |  |
| Address for Communication  |                     |  |  |  |  |  |
|  |                     |  |  |  |  |  |
| Mobile No.   |                     |  |  |  |  |  |
| Email:   |                     |  |  |  |  |  |
| Duration you are available for the position:                                     |                     |  |  |  |  |  |
| Name of The Post Applied :   |                     |  |  |  |  |  |
| Are you interested to register for Ph.D. at NIT Jalandhar, if selected: Yes/No.? |                     |  |  |  |  |  |

2. Details of School/University/Institute Studied (From 10<sup>th</sup>/MatriculationOnward)

| S.<br>No. | Degre<br>e | Discipline | University/<br>Institute | Regular/<br>Part-time | Year | %Marks<br>/CGPA* | Division |  |
|-----------|------------|------------|--------------------------|-----------------------|------|------------------|----------|--|
|           |            |            |                          |                       |      |                  |          |  |
|           |            |            |                          |                       |      |                  |          |  |
|           |            |            |                          |                       |      |                  |          |  |
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|           |            |            |                          |                       |      |                  |          |  |
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### 3. (i) Title of PG Thesis

(ii) Title of PhD Thesis (If applicable)

| 4. GATE/CSIR/UGC/LS-NE<br>Examination Details  |   | Branch  |  | Year   | Valid Up<br>to                                  |   | Percentile<br>(&Score)   |   | All India<br>Rank |
|--|---|---|--|--|---|---|--|---|-------------------|
|  |   |   |  |  |   |   |  |   |                   |
| 5. Professional Exp  | eriences (  | Teaching/Res  | search/I   | ndustria   | ıl) ifa   | ny  |  | I   |                   |
| Name of Organization   | Designa   | Designation   |  | Nature of Work   |   | From                                      |  | То  |                   |
|  |   |   |  |  |   |   |  |   |                   |
|  |   |   |  |  |   |   |  |   |                   |
|  |   |   |  |  |   |   |  |   |                   |
| 6. List of Research F  | ublication  | ı (ifany):  |  |  |   |   |  |   |                   |
| . Expertise (How to  | fit to imp  | lement the p  | roject):   |  |   |   |  |   |                   |
| 3. Any other Relevan   | nt Informa  | ation:  |  |  |   |   |  |   |                   |
| DECLARATION  |   |   |  |  |   |   |  |   |                   |
| I am willing to work<br>development of Sys<br>Functional Safety a<br>research under the g<br>particulars supplied<br>best of my knowledg<br>and regulations of the | tem on Cl<br>nd Securit<br>uidance of<br>by me and<br>ge and beli | hip based next<br>ty Features".<br>the Principal<br>d the entries of<br>ef. If selected | t general I under Investige Investige Investige In I for the | ation Iol<br>take to<br>gator of the<br>this app<br>position | Γ Sys<br>engag<br>he sch<br>olication,<br>I pro | tem for ge myse neme. I hon form omise to | Industry 4.0 lf whole timereby declar are correct abide by the | 0 with<br>ne fo<br>re tha<br>to the<br>e rule | n<br>r<br>t<br>e  |
| Place :  |   |   |  |  |   |   |  |   |                   |
| Date:  |   |   |  | Signat   | ture o  | f the Ap                                  | plicant  |   |                   |
| List of enclosures:  |   |   |  |  |   |   |  |   |                   |