

TEQIP-III SPONSORED

Short Term Course On

“Recent Trends in Biomedical Signal Processing and VLSI Design”

(RTBSPV - 19)

May 30-June 03, 2019

For Faculty/PG/PhD students Technical Institutes
& Industrial persons

Patron:

**Prof. (Dr.) Lalit K Awasthi
Director NIT Jalandhar**

Course Coordinators:

Dr. Ramesh Kumar Sunkaria
Dr. Ashish Raman

Organized by:



*Department of Electronics and Communication
Engineering, Dr B R Ambedkar National Institute
of Technology, Jalandhar - 144011, Punjab
www.nitj.ac.in*

About the Institute

Dr B R Ambedkar National Institute of Technology (NIT), Jalandhar was established in the year 1987 as Regional Engineering College, was given the status of National Institute of Technology in the year 2002, and functions under the aegis of Ministry of Human Resource development (MHRD), Government of India. As an Institute of National Importance, it imparts high quality technical education in Engineering, Technology and Science to produce competent technical manpower for the country.

Location

The institute is located on Amritsar bye pass at a distance of 12 km from Jalandhar bus stand 11 km from Jalandhar city railway station, 16 km from Jalandhar Cantt, railway station, 170 km from Chandigarh, 80 km from Raja Sansi International Airport, Amritsar and 375 km from Indira Gandhi International Airport, New Delhi.

About the Department

The Department of Electronics and Communication Engineering offers B.Tech in Electronics and Communication Engineering, two full time M.Tech program in Electronics and Communication Engineering and VLSI Design. Department also offers the Doctoral programmes in the field of Communication, VLSI Design and Bio-Medical Engineering. The department has established state-of-art laboratories with sophisticated equipments for undergraduate, post graduate and Ph.D research work. The department has an advanced VLSI lab sponsored by Ministry of Communication and IT under SMDP-C2SD and Medical Imaging and

Computational Modelling of Physiological Systems Lab. In the VLSI lab Cadance, Mentor graphic, Synopsys, and Xilinx softwares are available for synthesis /simulation work.

Objective of Course

Biomedical Signal Processing and Very Large Scale integrated Circuits (VLSI) design technology have evolved significantly modern Electronic Communication Systems Design and state of art health care system. There is a great need of manpower training in the area of VLSI and Signal Processing Field. The main objective of this course is to provide theoretical and practical knowledge of the recent trends available in the domain of VLSI and Digital Signal Processing Systems applied to medical signals. During this programme the scientists and academicians in their respective areas of expertise will discuss the current scenario on Biomedical, Signal Processing and their applications in VLSI Design. This course gives an opportunity for hands-on experience on the VLSI design based and signal processing based tools.

General Information:

- The candidate will be informed of his/her selection in advance.
- No course fee will be charged from the participants. However, course material, lunch and refreshment will be provided during the workshop.
- No TA/DA will be paid by the institute for attending the course.
- Stay in hostels can be arranged for the outstation candidates on payment basis if desired

Organizing Committee

All Deans and Associate Deans

All HODs

Dr R K Sarin, Professor

Dr Arun Khosla, Professor

Dr B S Saini, Professor

Dr Mamta khosla, Associate Professor

Dr Indu Saini, Assistant Professor

Dr Deepti Kakkar, Assistant Professor

Dr Neetu Sood, Assistant Professor

Dr Balwinder Raj, Assistant Professor

Dr Nitish Kashyap, Assistant Professor

Dr Pawan Verma, Assistant Professor

Dr Sateesh Kumar Awasthi, Assistant Professor

Dr Deepak Bharti, Assistant Professor

Dr Sukhwinder Singh, Assistant Professor

Dr Harsh Verma

Dr Anish Sachdeva

Dr Vinay Midha

Dr Harsh Kumar Manchanda, Associate Professor

Dr L P Singh, Associate Professor

Dr Harsh Kumar Manchanda, Associate Professor

Dr R R Sinha, Assistant Professor

Dr Aditya Prakash, Assistant Professor

Dr Praveen Malik, Assistant Professor

REGISTRATION FORM

TEQIP-III Sponsored

Short Term Course

On

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and VLSI Design”

May 30-June 03, 2019

Name:

Designation:

Age:

Male/Female:

Academic Qualification:

Specialization:

Teaching Experience:

Department:

Name of Organization and Address:

Accommodation required:

Signature & Date

Mobile no.:

Email:

RECOMMENDATION OF THE SPONSORING AUTHORITY

The applicant is hereby sponsored and will be permitted to attend the workshop on “Recent Trends in Biomedical Signal Processing and VLSI Design”, if selected.

Signature and Seal of Sponsoring Authority with
Date

Important Dates

Reporting Date and Time at the Institute:

Date: 30/05/2019 at 9.00 am.

Venue: NIT Jalandhar

***The complete registration form should reach by speed post or email latest by May, 25, 2019 at the following address:**

Corresponding Address:

Course Coordinators

Dr. Ashish Raman, Assistant Professor, Department of Electronics and Communication Engineering, G T Road Bypass, Dr. B R Ambedkar National Institute of Technology, Jalandhar, Punjab
Pin 144011

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Dr. Ramesh K Sunkaria, Associate Professor, Department of Electronics and Communication Engineering, G T Road Bypass, Dr. B R Ambedkar National Institute of Technology, Jalandhar, Punjab
Pin 144011

Phone: 0181-2690301-02, Ext 2607,

Fax: 0181-2690320, 2690932

Mobile: 09780979984, e-mail: sunkariark@nitj.ac.in

*The registration form and information can be download from the institute website. www.nitj.ac.in